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September 16, 2003

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To:	Ex. D. Kang	From:	Eric J. Robinson
Facsimile Number:	703-308-7382	Telephone No.	703-305-9147
Number of Pages (including cover sheet):			12

**Please Deliver to Examiner Kang**

Re: U.S. Serial No. 08/558,544

For: Device for Reading an Image Having a Common Semiconductor Layer  
Our Reference: 0756-1441

Pursuant to our telephone conversation, attached please find a copy of the Amendment filed August 18, 2003. As we discussed, I have corrected the claims to conform to proper Reissue amendment practice and regret any inconvenience from this oversight. Also, please let me know if you need any of the other supporting documents associated with this Amendment and I can also fax or hand carry those to your attention. For your convenience, I also attach a copy of our PTO stamped receipt card evidencing receipt of these documents.

  
Eric Robinson  
Reg. 38,285

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Attorney Docket No. 0756-1441

#4575  
Annett J.  
Yakobson  
9/23/03**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of: ) Group Art Unit: 2811  
Shunpei YAMAZAKI )  
Serial No. 08/558,544 ) Examiner: D. Kang,  
Filed: November 16, 1995 ) CERTIFICATE OF FACSIMILE  
For: DEVICE FOR READING AN IMAGE ) I hereby certify that this correspondence is being  
HAVING A COMMON ) transmitted via facsimile to 703-308-7722.  
SEMICONDUCTOR LAYER ) \_\_\_\_\_

**AMENDMENT**

Honorable Commissioner of Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Official Action dated March 17, 2003, please consider the following amendments and remarks in connection with the above-identified application.

**In the Claims:**

Please amend the claims as follows:

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7. (Amended) [The] A device [of claim 5] for sensing a light comprising a photoelectric conversion semiconductor device, an n-channel thin film transistor, and a p-channel thin film transistor over an insulating substrate having a blocking layer, the device produced by a process comprising the steps of:

forming the blocking layer on the insulating substrate;  
depositing a semiconductor layer on the blocking layer;  
forming at least first, second, and third semiconductor islands by patterning the semiconductor layer;